

FIG. 2

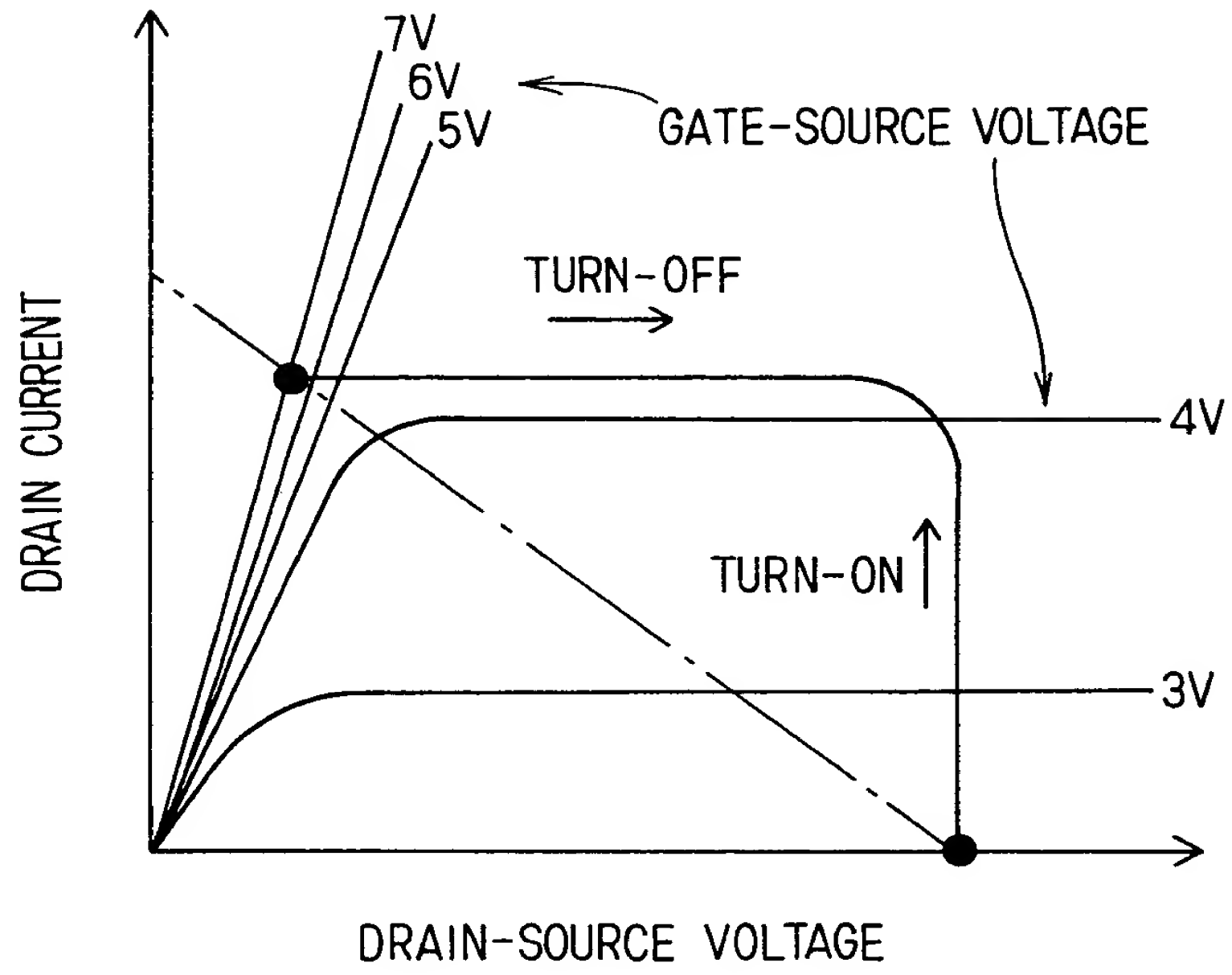


FIG. 3A

TRANSISTOR 8 ON

COMPARATOR 14
COMPARATOR 51
GATE CURRENT
GATE VOLTAGE
DRAIN CURRENT
DRAIN-SOURCE
VOLTAGE

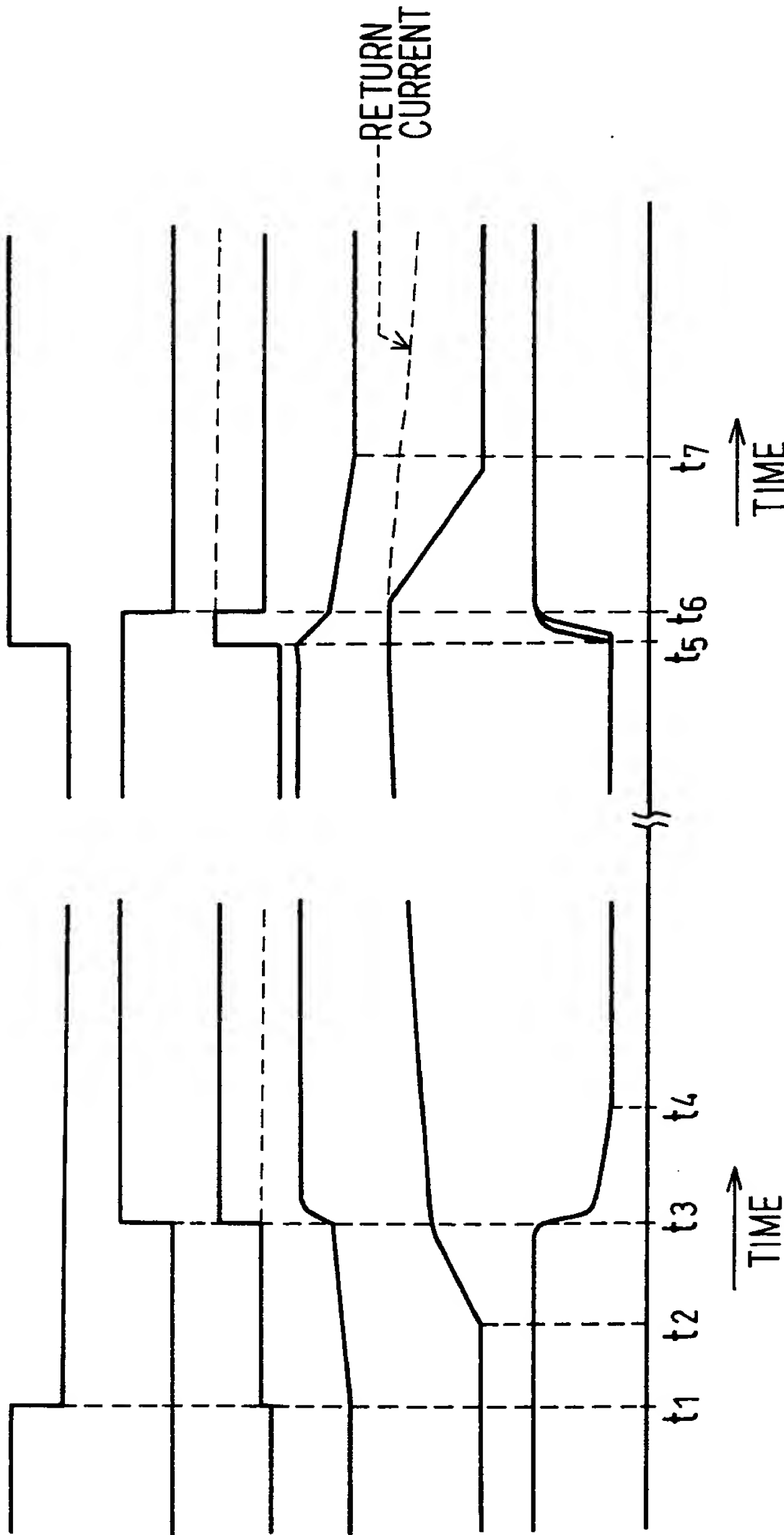
t₁ t₂ t₃ t₄
TIME

FIG. 3B

TRANSISTOR 8 OFF

RETURN
CURRENT

t₅ t₆ t₇
TIME



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FIG. 4

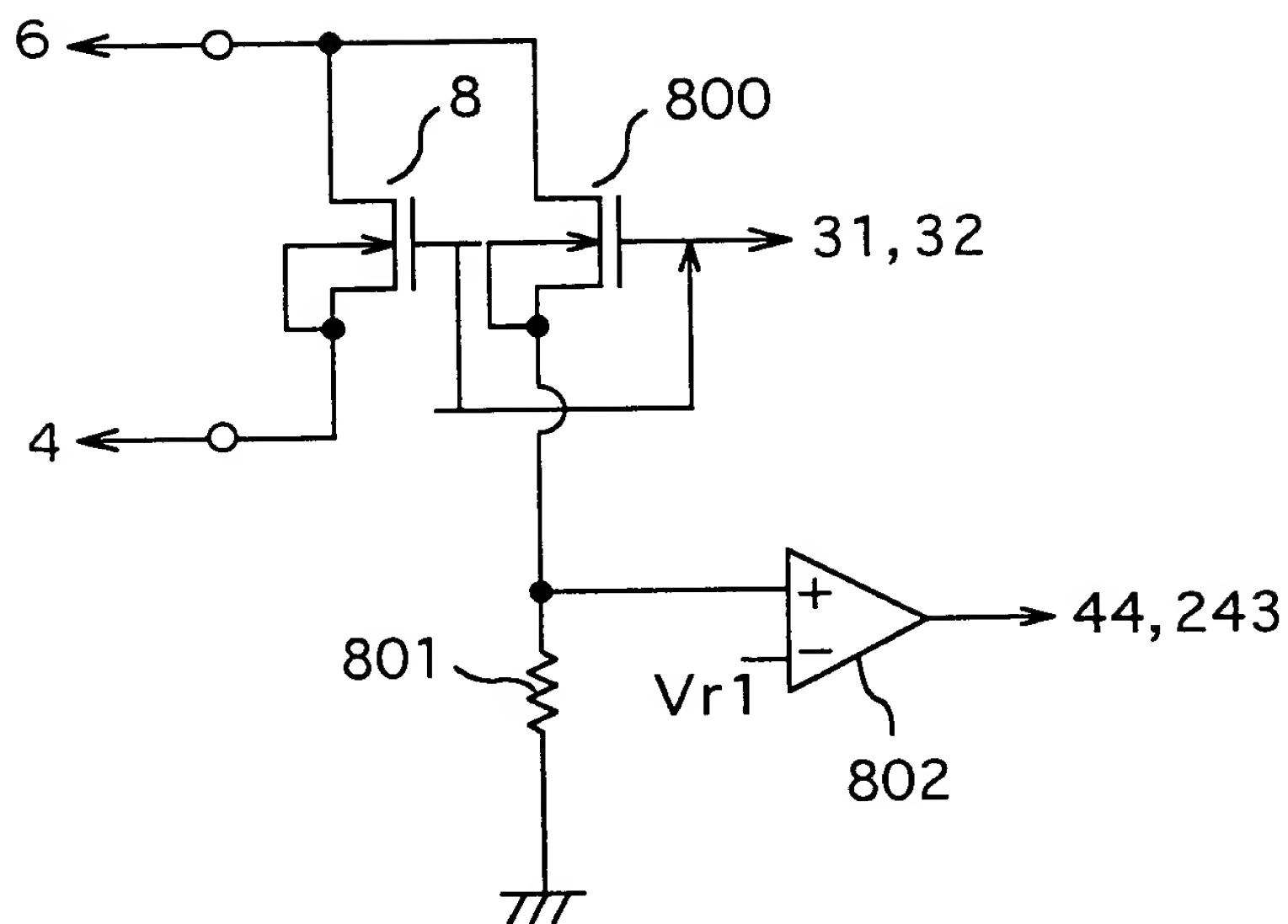


FIG. 5

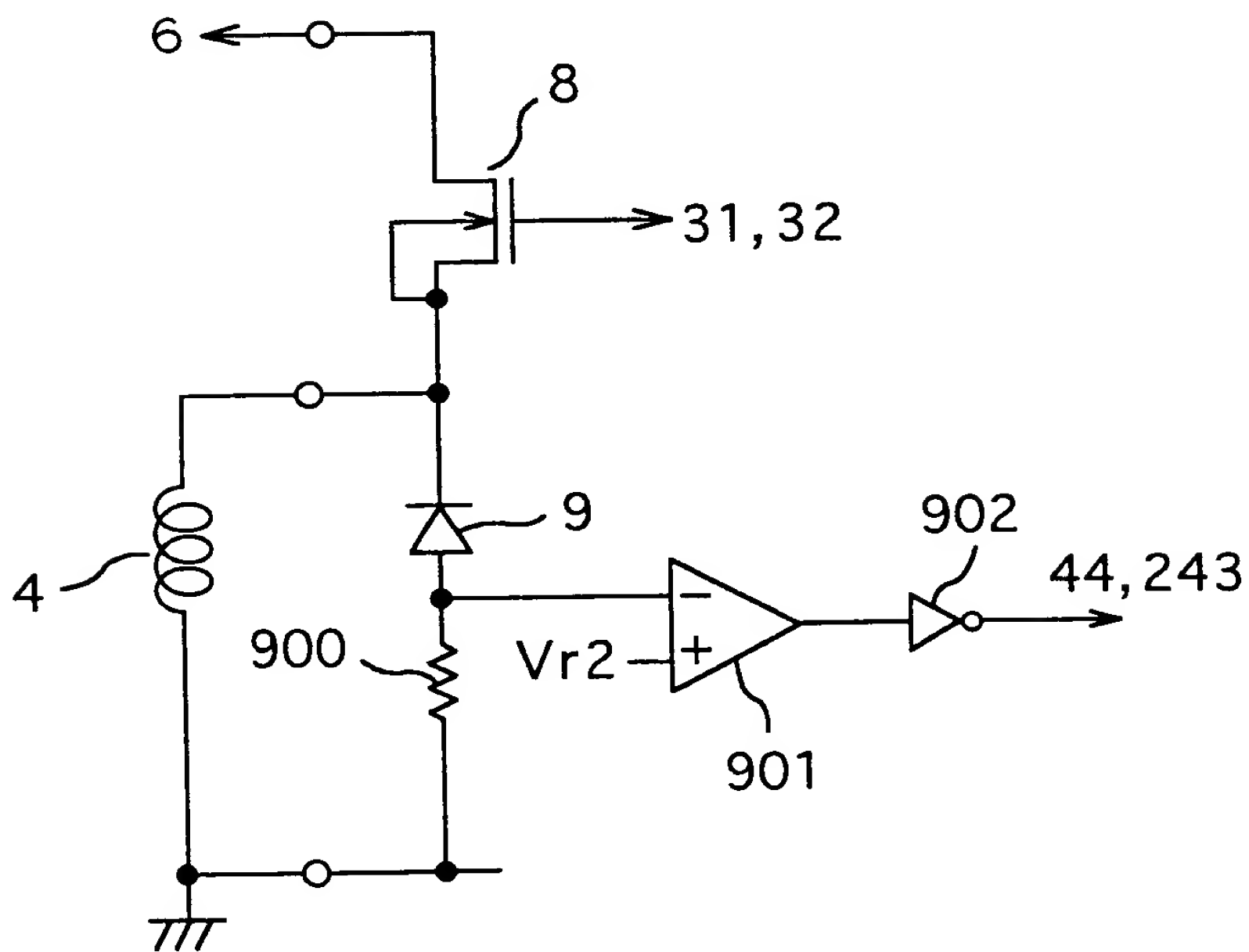
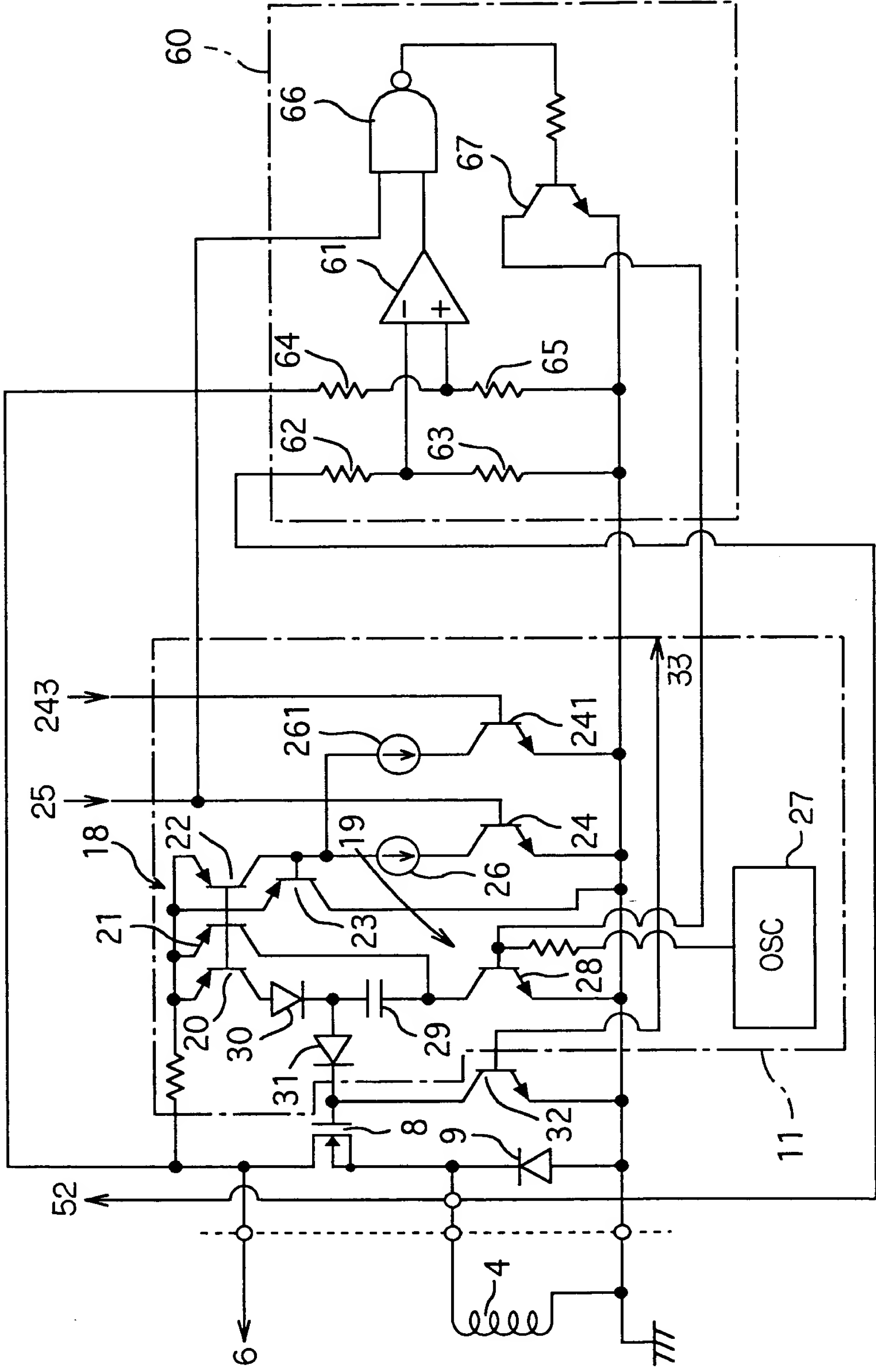


FIG. 6



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FIG. 7A

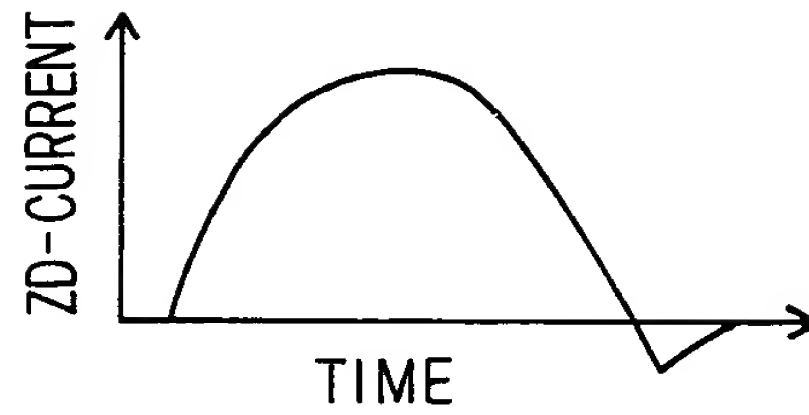


FIG. 7B

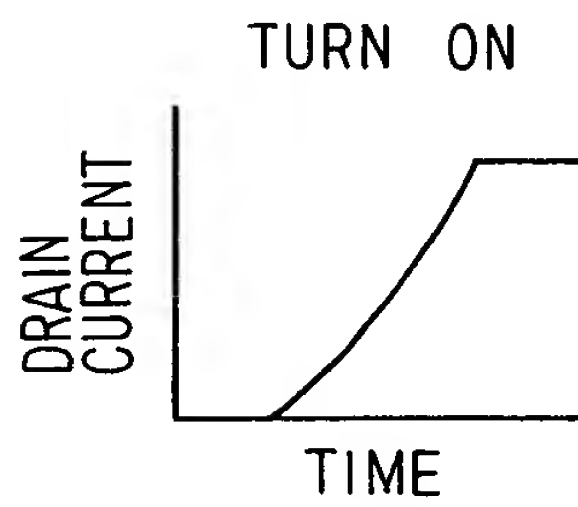


FIG. 7C

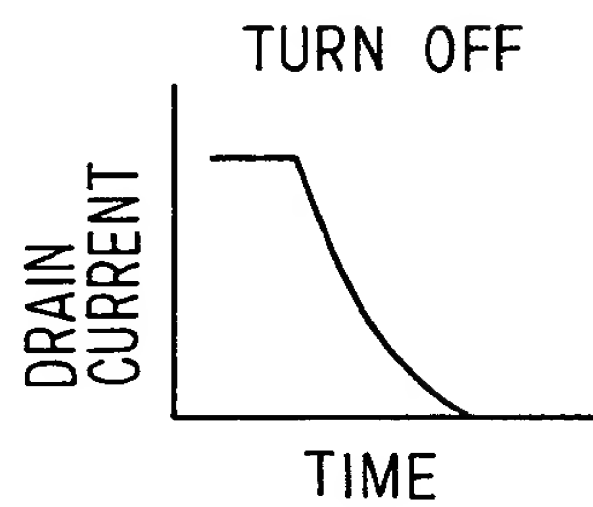


FIG. 8

